

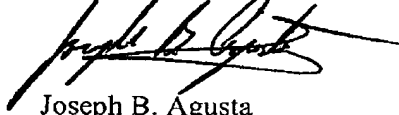
App!. No. 10/797,726  
Amdt. dated January 23, 2006

Please replace the Abstract of the application at page 544, line 2 with the following new paragraph:

A system core having an internal memory which transfers data from an external device to the internal memory is described. To this end, the system core includes a processor, a direct memory access (DMA) controller, an instruction memory and a plurality of memories. The instruction memory contains processor instructions and DMA instructions. The DMA controller fetches DMA instructions from the instruction memory. The DMA controller executes the fetched DMA instructions and thus populates the plurality of memories with data from the external device. The processor then operates on the data found in the populated memories.

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Respectfully submitted,



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